

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit 2662

Examiner Hassan Kizou

In Re: Enrique Musoll et al.
Case: P3828
Serial No.: 09/927,129
Filed: 08/10/2001
Subject: Context Selection and Activation Mechanism for Activating One of a Group of Inactive Contexts in a Processor Core for Servicing Interrupts

To: The Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Information Disclosure Statement under 37 C.F.R. §1.97

Enclosed herewith is an Information Disclosure Statement (IDS) by Applicant listing twenty nine (29) references. Under §1.98(d) no reference copies are required as they have been provided in priority application s/n 09/737,375. Applicant desires that the references be entered in the above-mentioned case as relevance dictates.

As prosecution has not begun in this case, in accordance with rule 1.97(b)(3), no additional fee is required. Please charge any deficiency or credit any overpayment to Deposit Account No. 50-0534.

Respectfully submitted,
Enrique Musoll et al.
by



Donald R. Boys
Reg. No. 35,074

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 3

Complete if Known

Application Number	09/927,129
Filing Date	08/10/2001
First Named Inventor	Enrique Musoll
Art Unit	2662
Examiner Name	Hassan Kizou
Attorney Docket Number	P3828

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 6088745 A *	07-11-2000	Bertagna et al.	
		US- 6157955 A *	12-05-2000	Narad et al.	
		US- 6169745 B1 *	01-02-2001	Liu et al.	
		US- 6226680 B1 *	05-01-2001	Boucher et al.	
		US- 5918050 A *	06-29-1999	Rosenthal et al.	
		US- 6223274 B1 *	04-24-2001	Catthoor et al.	
		US- 6023738 A *	02-08-2000	Priem et al.	
		US- 5659797 A *	08-19-1997	Zandveld et al.	
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		US- 4942518 A *	07-17-1990	Weatherford et al.	
		US- 4707784 A *	11-17-1987	Ryan et al.	
		US- 6009516 A *	12-28-1999	Steiss et al.	
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		US- 5987578 A *	11-16-1999	Butcher, Lawrence	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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		Number-Kind Code ² (if known)			
		US- 5471598 A *	11-28-1995	Quattromani et al.	
		US- 6263452 B1 *	07-17-2001	Jewett et al.	
		US- 5978570 A *	11-02-1999	Hillis, W. Daniel	
		US- 6073251 A *	06-06-2000	Jewett et al.	
		US- 6381242 B1 *	04-30-2002	Maher, III et al.	
		US- 5796966 A *	08-18-1998	Simcoe et al.	
		US- 5291481 A *	03-01-1994	Doshi et al.	
		US- 5121383 A *	06-09-1992	Golestani, S. Jamaloddin	
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				Examiner Name	Hassan Kizou
Sheet	3	of	3	Attorney Docket Number	P3828

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		* KNUTH, DONALD E., "The Art of Computer Programming, Third Edition, Volume 1, Fundamental Algorithms", "Sec. 2.5 Dynamic Storage Allocation", 1997, pp 435-456, Addison-Wesley, US	
		* DIEFENDORFF, Keith, K7 Challenges Intel, Microprocessor Report, October 26, 1998, Vol. 12, No. 14, US	

Examiner Signature		Date Considered	
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